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E. Willis  
8-25-00jc525 U.S. PTO  
09/535233  
03/24/00

Attorney Docket No. SEL 171

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION TRANSMITTAL LETTER

Transmitted herewith for filing is the patent application of:

1. Inventor(s): Masaya KADONO, Shunpei YAMAZAKI, Yukio YAMAUCHI and Hidehito KITAKADO
2. Title: Semiconductor Device And A Method Of Manufacturing The Same

Name of applicant(s) and current correspondence address of applicant(s)

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Enclosed are:

X 18 Sheets of Drawings  
       Formal  
X Informal

jc690 U.S. PTO  
03/24/00

03/24/00

**THE UNIVERSITY OF CHICAGO**

Serial no. 11-084989 filed March 26, 1999 in Japan.

## Claims as Filed

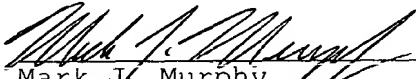
- 2 -

\_\_\_\_\_ Please charge my Deposit Account No. 50/1039 in the amount of \$ \_\_\_\_\_. A duplicate copy of this sheet is enclosed.

X The Commissioner is hereby authorized to charge any additional fees (except the issue fee) which may be required at any time during the prosecution of this application without specific authorization, or credit any overpayment to Deposit Account No. 50/1039. A duplicate copy of this sheet is enclosed.

X A check in the amount of \$ 690 is enclosed to cover the filing fee and the recordation of the Assignment, if any, transmitted herewith.

Date: March 24, 2000

  
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Date of Deposit March 24, 2000

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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Signature Cristine M. Noll